## Chapter 1. TurstZone

### Overview

Turstzone (TZC) is an AMBA compliant SoC peripheral. It performs security checks on transactions to memory or peripherals. You can use the TZC-400 to create up to eight separate regions in the address space, each with an individual security level setting. Any transactions must meet the security requirements to gain access to the memory or peripheral. You can program the base address, top address, enable, and security parameters for each region.

### 1.2 Block Diagram



Fig. 1.1 TZC functional block diagram

### 1.3 Features

* 6 TZC filters included inside Sirius
* The ability to define up to 8 address regions on the address map
* A default base region to cover all remaining portions of the address map
* Software programmable security access permissions for each address region through an APB4 interface
* Identity-based filtering of Non-secure accesses
* Support for 16 outstanding transactions on the normal paths